

U.S. Army Research, Development and Engineering Command



TECHNOLOGY DRIVEN. WARFIGHTER FOCUSED.

Advance Testing Capability (ATC)

DoD M&S Conference 10 March 2008

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Problem Space Overview



- Testing a federate in a distributed simulation environment is a 2 step process:
 - Federates are tested individually by their developers
 - The distributed simulation environment is then tested as a whole
- Data dependencies between federates force many groups to skip the individual testing and only test the system as a whole
- Problems in integration arise because federates are not tested for their adherence to system design before being integrated into the whole distributed simulation environment



Benefits Overview



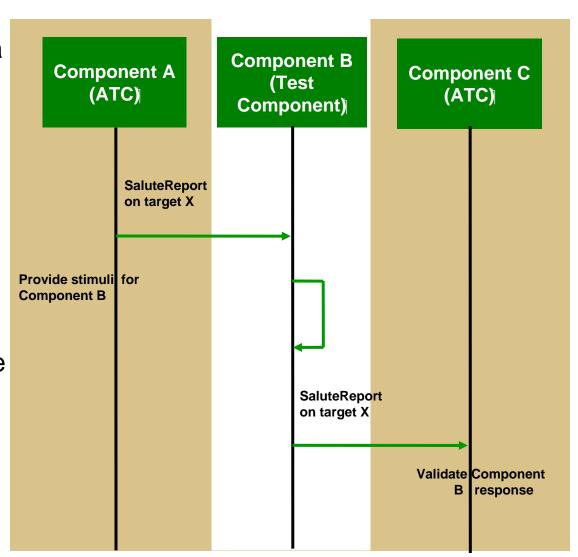
- Provides users the capability to build, store and execute test for components built on the MATREX tools
- Provides the capability to perform meaningful and repeatable blackbox testing on an individual components build on the MATREX tools
- Allows developers to test their individual components without having to bring up the entire federation, making debugging easier and lower the cost of testing
- Allows the Integration and Test team to debug issues during integration
- Can be used as an acceptance test for new and updated components



Features and Capabilities



- Allows the users to create a sequence of actions or events to stimulate the component under test and generate responses
- Validates the responses from the test component
- Generates source code which is then run to execute the test and verify results.





Future Work



- "OM-Agility"
 - Decouple ATC from the MATREX FOM
 - ➤ Allows the use of any Object Model
- MATREX SDR import
 - Ability to import requirements from MATREX SDR and produce an ATC Test Case.
 - > Allows test case traceability back to system level requirements
- "Live Interactive Mode"
 - ATC will become a ProtoCore component on the wire
 - Allows pair-wise and federation level testing
 - ➤ Allows registering objects, object updates and interactions at run-time.
 - ➤ Allows ATC to operate in various simulation architectures, including HLA 1.3, HLA 1516, TENA.



Customers



RDECOM (RDEC's)

- Aviation and Missile Research, Development and Engineering Center (AMRDEC)
- Armament Research, Development and Engineering Center (ARDEC)
- Army Research Laboratory (ARL)
- Communications-Electronics Research, Development and Engineering Center (CERDEC Belvoir/Monmouth)
- Edgewood Chemical Biological Center (ECBC)
- Natick Soldier Research, Development and Engineering Center (NSRDEC)
- Simulation & Training and Technology Center (STTC)
- Tank and Automotive Research, Development and Engineering Center (TARDEC)

FCS LSI



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BACKUP



ATC GUI



